

is created in said wafer, and that said sacrificial layer is subsequently removed.

10. The method according to Claim 1, characterized in that any residual contaminations possibly present, which remain on said wafer as a result of the creation of said deep pit in said wafer, are reduced or removed by a subsequent evaporating step using a second radiation pulse.
11. The method according to Claim 10, characterized in that the beam parameters for removal or reduction of said residual contaminations are matched.
12. The method according to Claim 1, characterized in that a further radiation pulse is generated as a further step of operation, which, compared against said first radiation pulse, has a lower energy density and causes the wafer material to commence fusing.
13. The method according to Claim 1, characterized in that a plurality of mutually spaced deep pits is created.
14. The method according to Claim 13, characterized in that a pit is created by means of said first radiation pulse and that the pit so created is subjected to the action of said further radiation pulse prior to the creation of a further pit.
15. The method according to Claim 13, characterized in that initially each of said holes or pits is created by means of said first radiation pulse and that only then all holes or pits are each subjected to the action of said further radiation pulse.
16. The method according to Claim 1, characterized in that the depth of said pit is within the range between 3 μm and 10 mm.
17. The method according to Claim 16, characterized in that the

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24. The wafer scribing device according to Claim 18, characterized in that said beam generator means comprises at least one laser.
25. The wafer scribing device according to Claim 18, characterized in that one fraction of said radiation pulse can be masked out.
26. The wafer scribing device according to Claim 25, characterized in that a central, particularly circular, portion of said radiation pulse can be masked out by means of an aperture.
27. A system for manufacturing semiconductor devices, specifically semiconductor processors, including a wafer scribing device according to claim 1.
28. A scribing method for wafers, wherein a defined beam is directed onto said wafer by means of a beam generator means so as to remove some wafer material from a wafer region, characterized by the further step of generating at least one of a first radiation beam and a second radiation beam having a predeterminable energy density and used to create a deep pit in said wafer, whereas the pit is deep enough to remain a pit throughout subsequent manufacturing steps of said wafer and whereas the edge of the pit is smooth.
29. The method of claim 28, characterized in that said second radiation beam smoothes the edge of the pit.
30. The method of claim 28, characterized in that the first and/or the second radiation beam is at least a pulse.

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